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TITLE:	Method for performing inter processor communication using dual port ram in cdma system

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PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
KR 2003084005 A	November 1, 2003	N/A	001	H04Q 003/54

APPLICATION-DATA:

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ABSTRACTED-PUB-NO: KR2003084005A

BASIC-ABSTRACT:

NOVELTY - A method for performing IPC(Inter Processor Communication) using a dual port RAM in a CDMA system is provided to combine benefits of an existing interrupt system with benefits of a polling system during the IPC, and to perform DPRAM communication, thereby preventing IPC interruption by preventing interrupt loss.

DETAILED DESCRIPTION - The first processor(100) generates an interrupt while setting a flag to an 'interrupt generation' state. If the first processor(100) wants to re-generate the interrupt, the first processor(100) checks the flag and re-generates the interrupt only when the flag is set to a 'processing completion' state. The second processor(200) sets the flag to the 'processing completion' state after completing the processing of all interrupts, so that the first processor(100) generates the interrupt.

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DRAWING:
TITLE-TERMS: METHOD PERFORMANCE INTER PROCESSOR COMMUNICATE DUAL PORT RAM CDMA SYSTEM

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